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**Information Disclosure
Statement By Applicant**
(Use as many sheets as necessary)

Application Number	10/788,857
Filing Date:	February 26, 2004
First Named Inventor:	Chao I. WU
Art Unit:	2824
Examiner Name:	N. Nguyen
Attorney Docket Number:	MXICP031

Patent Application Sheet

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U.S. Patent Documents

Examiner Initial	Cite No.	Document No.	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
NN	A	6,011,725	1/2000	Eitan	
NN	B	6,487,114	11/2002	Jong et al.	
C					
D					
E					
F					
G					
H					
I					
J					
K					

Foreign Patent Documents

Examiner Initial	Cite No.	Foreign Patent Document No.	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear	Translation
						YES NO
L						
M						
N						
O						
P						
Q						

Non Patent Literature Documents

Examiner Initial	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
NN	R	T. Y. CHAN et al., "A True Single-Transistor Oxide-Nitride-Oxide EEPROM Device," IEEE Electron Device Letters, Vol. EDL-8, No. 3, March 1987, pp. 93-95
S		
T		
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Examiner Signature Nam Nguyen Date Considered 2/24/06

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